Low Power Scheduling for High-Level Synthesis

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*Abstract*—This paper explains how low power scheduling is used for High-Level Synthesis . Scheduling is one of many stages for high-level synthesis. By using low power scheduling, switching activity is reduced which meet the time constraints. More details about scheduling are written in this paper, so that we clearly understand the benefits of low power scheduling for High-Level Synthesis. (*Abstract*)

Keywords—component, formatting, style, styling, insert (key words)

# Introduction (*Heading 1*)

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1. G. Eason, B. Noble, and I. N. Sneddon, “On certain integrals of Lipschitz-Hankel type involving products of Bessel functions,” Phil. Trans. Roy. Soc. London, vol. A247, pp. 529–551, April 1955. *(references)*
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7. M. Young, The Technical Writer’s Handbook. Mill Valley, CA: University Science, 1989.

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